

A-Series PCIe Power Interposer Card

OGT-AP100

The A-Series PCIe Power Interposer Card for OakGate Appliances and the 7-Slot PCIe Enclosure (OGT-AP100) provides power control and measurement for an attached PCIe storage device, such as a PCIe edge card typically. With the proper adapter (SFF-8639 adapter), other types of PCIe devices can be attached as well. The OGT-AP100 resides in a PCIe backplane slot of a supported OakGate appliance or enclosure.



Figure 1. PCIe Power Interposer Card for OakGate Appliances and 7-Slot PCIe Enclosure (OGT-AP100)

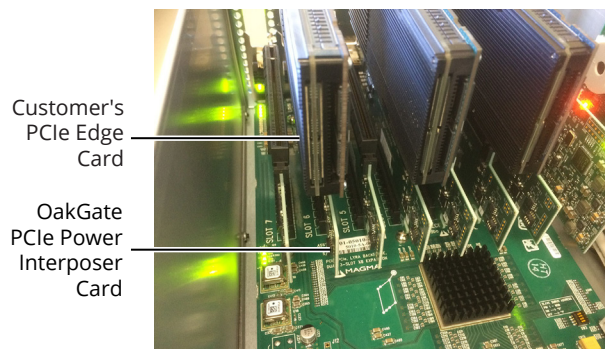


Figure 2. Backplane of a 7-Slot PCIe Expansion Enclosure

SPECIFICATIONS	
Form Factor:	Low Profile
PCIe Slot:	PCIe Gen3 x8 or x16 lane
Dimensions:	7.5" W × 2.0" H

ORDERING

PRODUCT DESCRIPTION	MODEL NUMBER
A-SERIES POWER INTERPOSER CARD	
PCIe Power Interposer Card for OakGate Appliances and 7-Slot PCIe Enclosure	OGT-AP100
COMPATIBLE APPLIANCES & ENCLOSURE	
Compact Desktop Appliance (January 31, 2018 or later)	OGT-DC100
2U Rackmount Appliance	OGT-R200
3U Rackmount Appliance	OGT-R300
7-Slot PCIe Gen3 Expansion Enclosure	OGT-EP701

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PERIPHERAL CAPABILITIES

- Power On/Off
- Power Measurement for +3.3V and +12V
- Power Cycle without PERST
- Assert PERST During Entire Power Cycle[†]
 - PERST is asserted ~1 second before power goes down
 - PERST is deasserted ~150 milliseconds (ms) after power is up
- Assert PERST During Power On Only[†]
 - PERST is asserted ~10 ms before power is applied to the device
 - PERST is deasserted ~150 ms after power is up
- Assert/Deassert PERST with Independent Control[†]
 - When PERST is asserted and power is being applied to the device, then a warm reset occurs and is held. A warm reset is essentially a fundamental reset with power still being applied.
 - When PERST is deasserted and power is being applied to the device, the warm reset is released.
 - Assert Pin Timer
 - Deassert Pin Timer

[†] Requires OGT-AP100 firmware rev 4 and SVF v3.8.2, or later.